

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	819	716/16.cds.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:58
L4	497	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L5	201	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L6	27	(L4 or L5) and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L7	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L8	46	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L9	73	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

L10	69734	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L11	12	L9 and L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L12	218	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L13	45	L10 and L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L14	23	L10 and (microcontroller or microprocessor) near5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L15	4403	713/1.ccls. or 713/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L16	334	L10 and L15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L17	3851	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

L18	241	L10 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L19	1224	380/44.ccls. or 380/45.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L20	9311	L15 or L17 or L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L21	497	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L22	77	L20 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L23	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L24	3	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L25	8	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

L26	201	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L27	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L28	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L29	80	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deny\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L30	15	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
L31	36	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S1	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:47
S2	33	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08

S3	67	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:48
S4	54741	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:49
S5	12	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:52
S6	186	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:52
S7	36	S4 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:59
S8	22	S4 and (microcontroller or microprocessor) near5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:00
S9	3787	713/1.ccls. or 713/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:03
S10	261	S4 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:04

S11	3035	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:05
S12	174	S4 and S11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:05
S13	1042	380/44.ccls. or 380/45. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08
S14	7743	S9 or S11 or S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08
S15	367	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:09
S16	59	S14 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:55
S17	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 14:12
S18	2	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/25 16:30

S19	7	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/27 09:52
S20	177	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/27 09:52
S21	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 12:42
S22	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 12:46
S23	80	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deny\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 13:22
S24	15	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 14:09
S25	36	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 14:09

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